

IN THE CLAIMS:

Amend claims 1-3 and 7-9 as follows:

1. (Currently Amended) An equalizer arrangement for generating an output signal by equalizing an input signal, comprising:

at least two mutually interfering first equalizer sections; and

at least two correcting second equalizer sections, wherein each of the second equalizer sections is uniquely associated with an associated one of the first equalizer sections which has the same center frequency, and each of the second equalizer sections has an equalization response which at least partially compensates for interference between the first equalizer sections, where

each of the second equalizer sections at its center frequency has a gain that is equal to the negative sum of the gain of at least one of the ~~plurality~~ first equalizer sections adjacent to the first equalizer section associated with the associated second equalizer section.

2. (Currently Amended) The equalizer of claim 1, where the gain of a certain of the second equalizer sections is equal to the negative sum of the gains of the first equalizer sections that are immediately adjacent to the first equalizer section that is uniquely associated with the certain of the second equalizer sections, wherein the gains are taken at the center frequency of the first equalizer section that is uniquely associated with the certain of the second equalizer sections.

3. (Currently Amended) The equalizer of claim 1, wherein the gain of the certain of the second equalizer sections is equal to the negative sum of the gains of the first equalizer sections that are adjacent to the first equalizer section that is uniquely associated with the certain of the second equalizer sections, wherein the gains are taken at the center frequency of the first equalizer section that is uniquely associated with the certain of the second equalizer sections.

4. (Previously Presented) The equalizer of claim 2, where the at least two mutually interfering first equalizer sections are arranged in serial and upstream of the at least two correcting second equalizer sections, where the at least two correcting second equalizer sections are also arranged in serial.

5. (Previously Presented) The equalizer of claim 1, where the at least two mutually interfering first equalizer sections, and the at least two correcting second equalizer sections include discrete time domain filters.

6. (Previously Presented) The equalizer of claim 1, where the at least two mutually interfering first equalizer sections, and the at least two correcting second equalizer sections include continuous time domain filters.

7. (Currently Amended) An equalizer that receives an input signal, comprising:

a first equalizer, including a first equalizer section having a gain G_1 that receives the input signal and provides a first equalizer output signal to a second equalizer section having a center frequency f_2 , where the second equalizer section provides a second equalizer output signal to a third equalizer section having a gain G_3 that provides a third equalizer output signal; and

a second equalizer having a first correcting equalizer section that receives a signal indicative of the third equalizer output signal and provides a first correcting equalizer output signal to a second correcting equalizer section that provides a second correcting equalizer output signal to a third correcting equalizer ~~output~~ section, which provides a third correcting equalizer output signal, where the second correcting equalizer section includes a gain value that is indicative of the negative

sum of the gains G_1 and G_3 associated with the first and third equalizer sections at the center frequency f_2 of the second equalizer section.

8. (Currently Amended) The equalizer of claim 7, where each of the ~~sections of the first and second equalizers~~ sections includes a bandpass filter.

9. (Currently Amended) An equalizer that receives an input signal, comprising:

a first equalizer, including a first equalizer section having a gain and that receives the input signal and provides a first equalizer output signal to a second equalizer section having a center frequency f_2 , where the second equalizer section provides a second equalizer output signal; and

a second equalizer having a first correcting equalizer section that receives a signal indicative of the second equalizer output signal and provides a first correcting equalizer output signal to a second correcting equalizer section that provides a second correcting equalizer output signal, where

the second correcting equalizer section includes a correction gain value that is indicative of the negative of the gain of the first equalizer section at the center frequency f_2 of the second equalizer section.